Yuan-Chun Lo

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Research experience

- ✧ Developed strategies for high power Terahertz (THz) sources, THz emitters, and THz communication.
- ✧ Explored interaction between EM waves and materials.
- ✧ Designed computer architecture for low-power deep neural nets.

Education

- ✧ University of National Tsing Hua 2014-9 ~ present
- ✧ Major: Electrical Engineering Average GPA 4.05

Experience

THz optoelectronic devices research 2017-7 ~ present

YRG lab, National Tsing Hua University (NTHU)

- ✧ Applied plasma physics to convert EM waves more efficiently to current.
- ✧ Developed THz antenna array and THz communication.
- ✧ Optimized carrier transport efficiency using solid state physics.
- ✧ Nanofabrication operation experience.

CNN low-power computation strategies 2016-9 ~ 2017-8

Under guidance of prof. Ren-Shuo Liu, NTHU

- ✧ Designed strategies to enhance computational efficiency in hardware to support CNN.
- ✧ Skills include image processing, usage of trained models, and circuit block-diagram design.

Training in FLYTECH 2017-8

A point-of-service company with 12% market share worldwide

- ✧ Developed strategies for future products with people in different background.
- ✧ Explored cross-department cooperation details from directors of R&D, marketing, purchasing, accounting, and finance departments.
- ✧ Won the best presentation prize reviewed by the chairman of the board, the general manager, and vice general managers.

President of student association 2016-6 ~ 2017-6

Department of Electrical Engineering (EE), NTHU

- ✧ Received Electronics Engineering students and advisors from City University of Hong Kong.
- ✧ Represented EE, NTHU and introduced it to high school students in Taiwan.
- ✧ Held project contests for all students in EE, NTHU.

Award

Excellent intern award in FLYTECH 2017-8

Voted by all interns

Excellent-EECS student 2017-5

Top 10% of all students in EE and Computer Science (CS), NTHU

Oversea experiencing award and scholarship 2016-2

25% of applications are approved with USD 3100

Outstanding award in the Department of EE 2015-10

Top 5% of all students in EE, NTHU

Additional training and courses

Deep Learning Circuits & Systems Design Technology 2017-8

- ✧ Improved hardware efficiency to support DNN.

Fabrication training in NTHU 2017-7

- ✧ Practiced ebeam, PECVD, etc.

Workshop held by MICRO conference 2016-10

- ✧ Investigated Chip architecture for deep neural network.

Summer program in UC Berkeley 2016-7 ~ 2016-8

- ✧ Analyzed business cases, discussed marketing strategies, and interchanged ideas with Korean, European, and the USA students.