# **CHIEH LEE**

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## **Education**

 National Tsing Hua University Major: Electrical Engineering

### **Research** Experience

Memory Circuit Design

June 2016-present

July, 2017-August, 2017

September 2014 – present

Professor: Meng-Fan Chang

- Topic: IGZO , Circuit Design of New type memory
- Simulated the circuit and do the analysis to modify or design a new circuit

## Work Experience

#### • Summer Intern

Industry Technology Research Institute

- Design a Portable UVC LED Water Disinfection system
- Make the prototype, do the experiment, and modify the design to let the system have high disinfection rate

## Administrative assistance June, 2016-present

Center of Teaching and Learning Development in National Tsing Hua University

- Assist Activities
- Make relationship with different kinds of people

# Scholarship

•	We	tech Qualcomm Global Scholars Program	January,2017 - present	
	•	One of the sixteen winners of Taiwan		
	•	6-month mentorship, make myself more marketable,	, enhancing my	

- competitive edge
- TSMC Scholarship of research assistance,2017 April,2017
  - Reward for research project

# Honor & Leadership

• Vice Coordinator Electrical Engineering camp October 2014 – September 2015

	•	Plan the activity sched	or		
	Deal with accounting affairs				
•	Student Association			June 2016 – June,2017	
	Electrical Engineering Department				
	Hold activities				
	Design posters for activities				
•	Event General Coordinator     Aboriginal Camp		itor	September 2015 - December 2015	
	•	Supervise and manage	activities progress		
Skills	5				
•	Hsp	ice •	Verilog	Solidworks	
•	Illus	strator	C/C++	<ul> <li>Matlab</li> </ul>	
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# Language

- English: fluently
- Chinese: fluently (native)